Pocket No.: 060188-0861 **PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Customer Number: 53080

Satoshi TAMURA, et al.

Confirmation Number: 8403

Application No.: 10/849,823

Group Art Unit: 2891

Filed: May 21, 2004

Examiner: Matthew L. Reames

For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICES

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The reference JP 2004-88083 cited on the attached form PTO-1449 claims priority of JP 2002-183919, and its relevance is discussed in the present specification.

10/849,823

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Facsimile: 202.756.8087 **Date: February 2, 2007**

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.